



GP 2835

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re Application of:

Warren M. Farnworth

Serial No.: 09/548,942

Filed: April 13, 2000

For: METHOD AND APPARATUS OF  
INTERPOSING VOLTAGE REFERENCE  
TRACES BETWEEN SIGNAL TRACES IN  
SEMICONDUCTOR DEVICES

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 4161US (98-1265)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

June 29, 2000  
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Kenneth C. Booth  
Typed/printed name of person whose signature is contained above

PRELIMINARY AMENDMENT

Box NON-FEE AMENDMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

Please revise the above-identified application as follows:

IN THE SPECIFICATION:

On page 2, line 22, change "increases" at the beginning of the line to --increase--;

On page 3, line 23, delete the comma after "2";

On page 4, line 1, change "the ground" to --each grounding--;

On page 4, line 15, change "multi-layer" to --multilayer--;

RECEIVED  
AUG 31 2000  
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